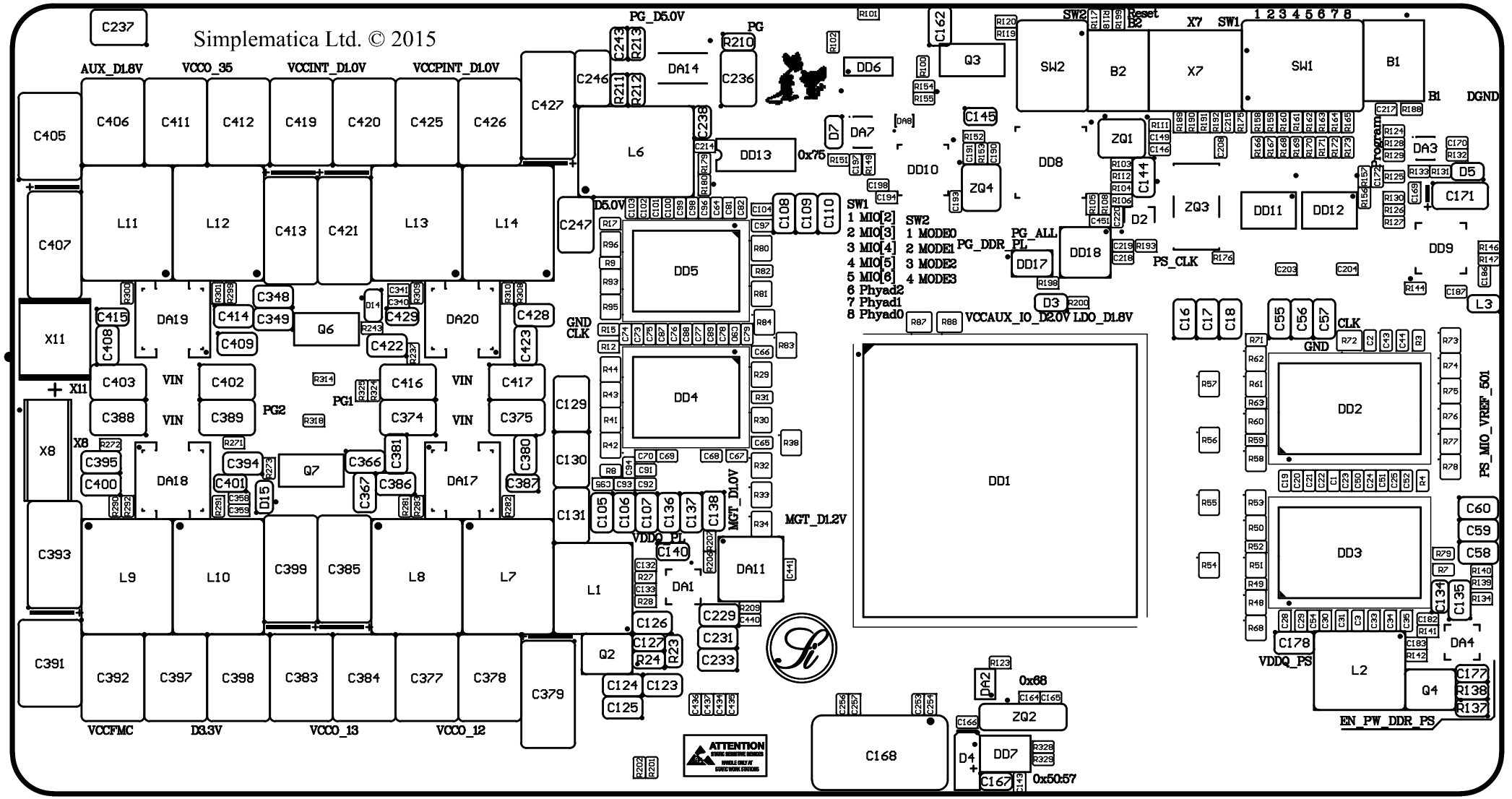
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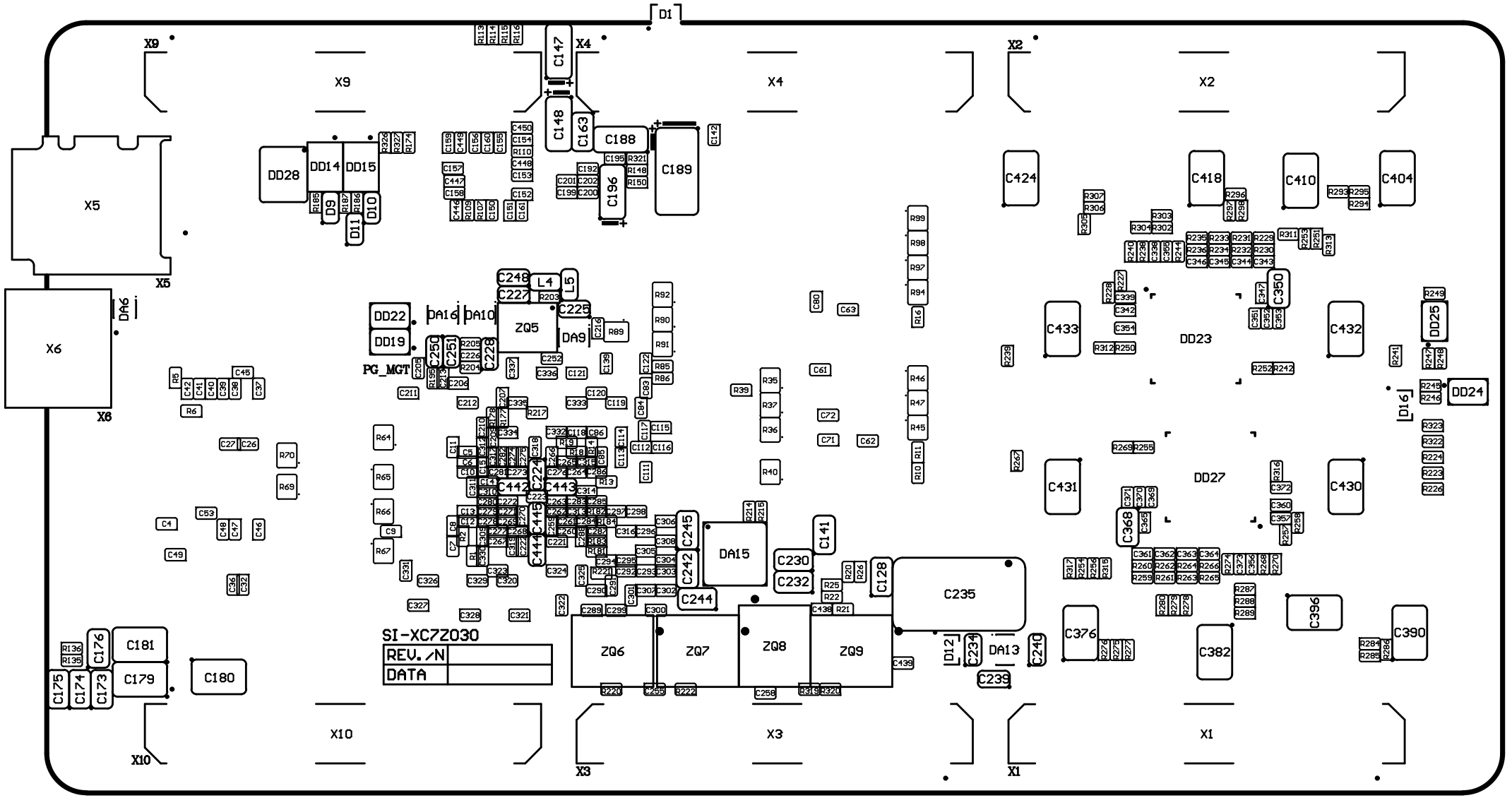
AUX_D1.8V
VCCO_35
VCCINT_D1.0V
VCCPINT_D1.0V
PG_D5.0V
PG
DGND
Reset
B2
X7
SW1
SW2
Program
D5.0V
GND
CLK
VIN
PG1
PG2
X11
X8
SW1
1 MIO[2]
2 MIO[3]
3 MIO[4]
4 MIO[5]
5 MIO[6]
6 Phyad2
7 Phyad1
8 Phyad0
SW2
1 MODE0
2 MODE1
3 MODE2
4 MODE3
PG_DDR_PL
PG_ALL
PS_CLK
VCCAUX_IO_D2.0V
LDO_D1.8V
MGT_D1.0V
MGT_D1.2V
VDDQ_PL
PS_MIO_VREF_501
VDDQ_PS
EN_PW_DDR_PS
VCCFMC
D3.3V
VCCO_13
VCCO_12
0x75
0x68
0x50:57
ATTENTION


SI-XC7Z030
REV. /N
DATA
PG_MGT
X9
X4
X2
X5
X6
X10
X3
X1
D1
DD28
DD14
DD15
DD22
DD19
DD23
DD27
DD24
DD25
ZQ5
ZQ6
ZQ7
ZQ8
ZQ9
DA9
DA15
DA13
DA16
DA10
DA6
D12
D16
C147
C148
C163
C188
C189
C196
C424
C418
C410
C404
C433
C432
C431
C430
C350
C368
C376
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C396
C390
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C179
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C173
C248
C227
C225
C250
C251
C228
C442
C443
C445
C444
C245
C242
C244
C230
C232
C141
C128
C235
C234
C239
C240
L4
L5